CHAROTAR UNIVERSITY OF SCIENCE AND TECHNOLOGY
CHANDUBHAI S PATEL INSTITUTE OF TECHNOLOGY
DEPARTMENT OF EC ENGINEERING
ACADEMIC YEAR: 2019-20
Faculty Course Review

Name of Teacher: Arpita Patel **Date:30/3/2020**
Subject: Digital System Design

No	Suggestions:
1.	Design for Testability should be included in more detail with back end vlsi flow
2.	More focus should be given on VERILOG
3.	– NIL –
4.	– NIL –
5.	– NIL –
6.	– NIL –

No	Feedback over the Content
1.	Students need more practice on Verilog programs
2.	Fundamental of digital electronics should be clear
3.	– NIL –
4.	– NIL –
5.	– NIL –
6.	– NIL –